

OUTPUT DRIVER IMPEDANCE CONTROL FOR ADDRESSABLE MEMORY DEVICES

Abstract

A selectable function is provided that permits the impedance of an output driver or an addressable memory device to be configured without adding extra signal connections. The output driver impedance control function of the invention is achieved through the use of the data bus of a memory array for control. The data lines thus serve two purposes one for normal use and the other for control of the impedance. In the invention, the output impedance of each DRAM in a subassembly array that drives a common data bus is individually separately adjusted.